FIG. 1A

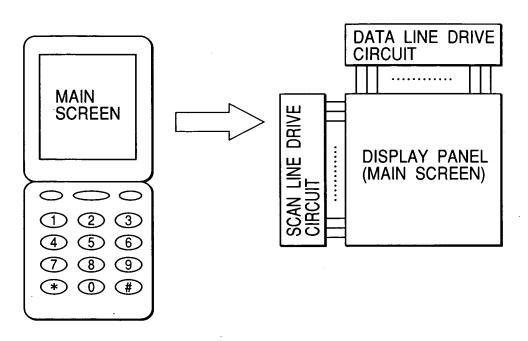
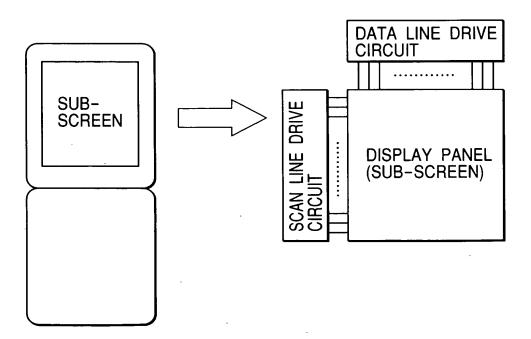
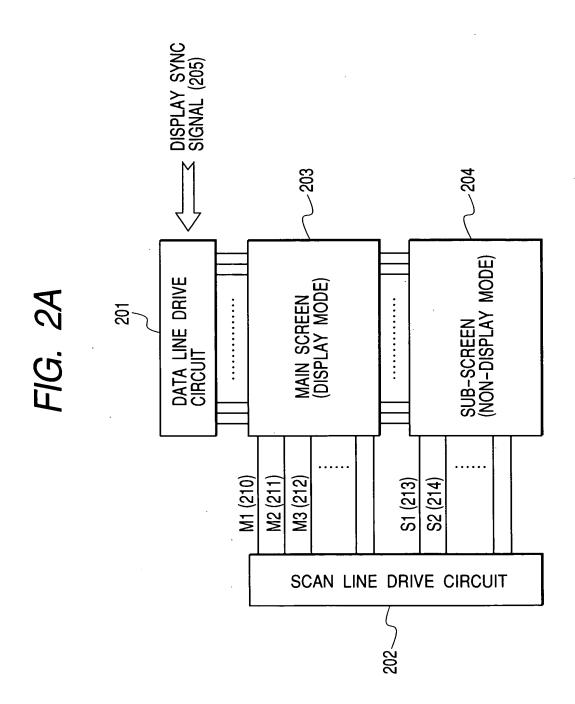


FIG. 1B





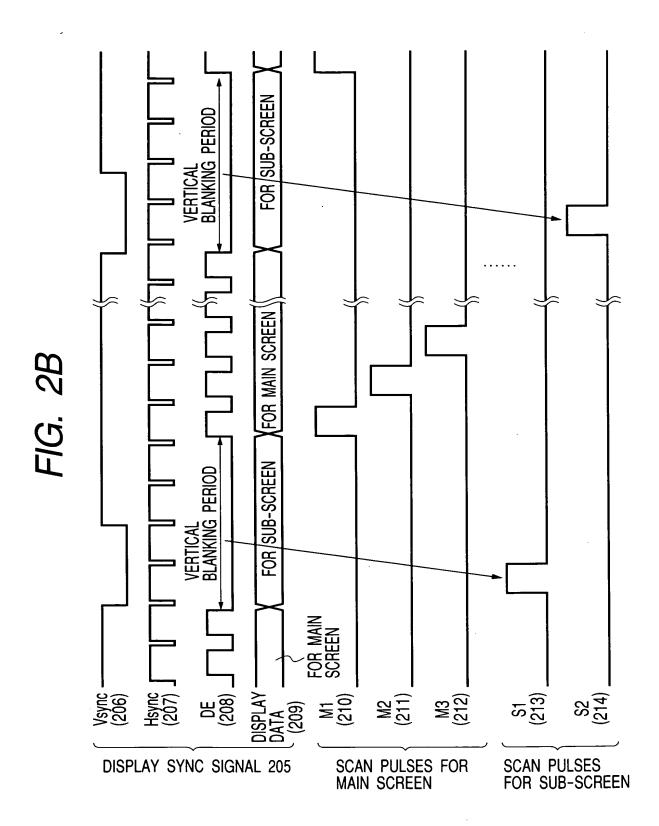
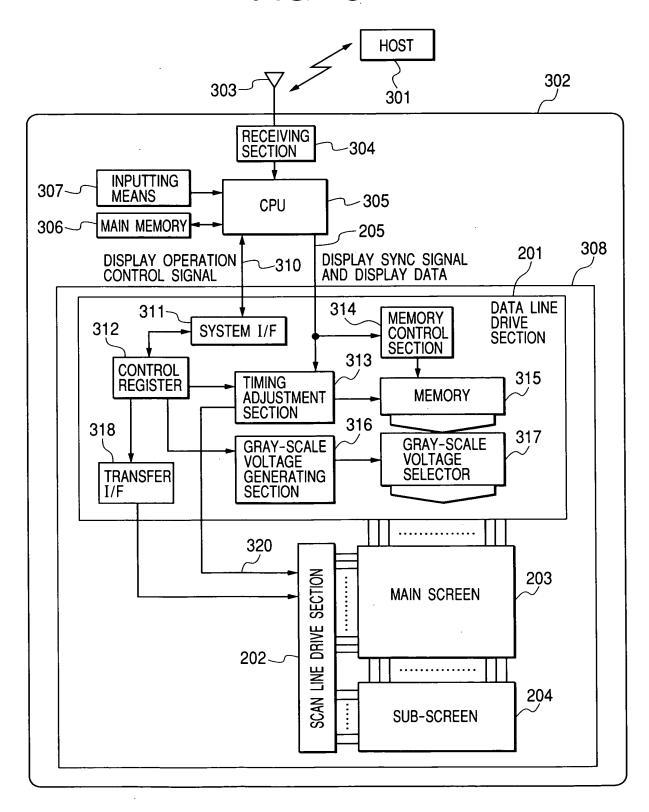


FIG. 3



CONTROL REGISTER		•					16	16-BIT DATA	DATA							
Address (Hex)	15	14	13	12	11	10	6	8	7	9	5	4	3	2	+	0
40+	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	DS
5	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	0
4 4 4	SL7	SL6	SL5	SL4	SL3	SL2	SL1	SL0	ML7	ML6	ML5	ML4	ML3	ML2	ML1	MLO
<b>=</b>	0	-	-	0	0	0	0	0	-	0	-	+	0	0	0	0
40+	*	*	*	*	*	*	*	*	BL7	BL6	BL5	BL4	BL3	BL2	BL1	BL0
[]	*	*	*	*	*	*	*	*	0	0	0	0	0	1	0	0
13h																

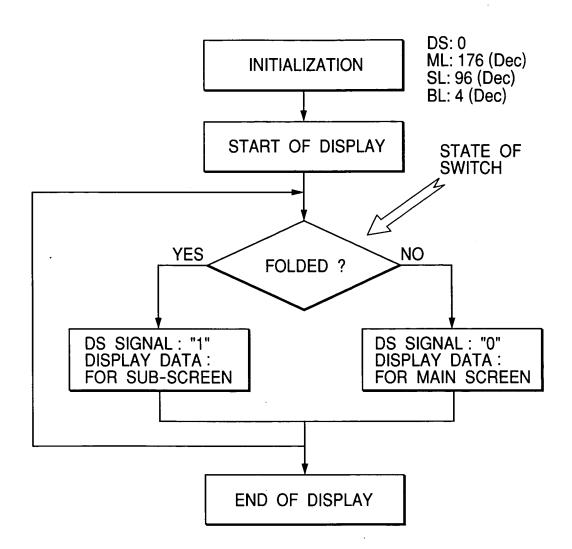
DS = SIGNAL FOR SELECTING DISPLAY-MODE SCREEN,

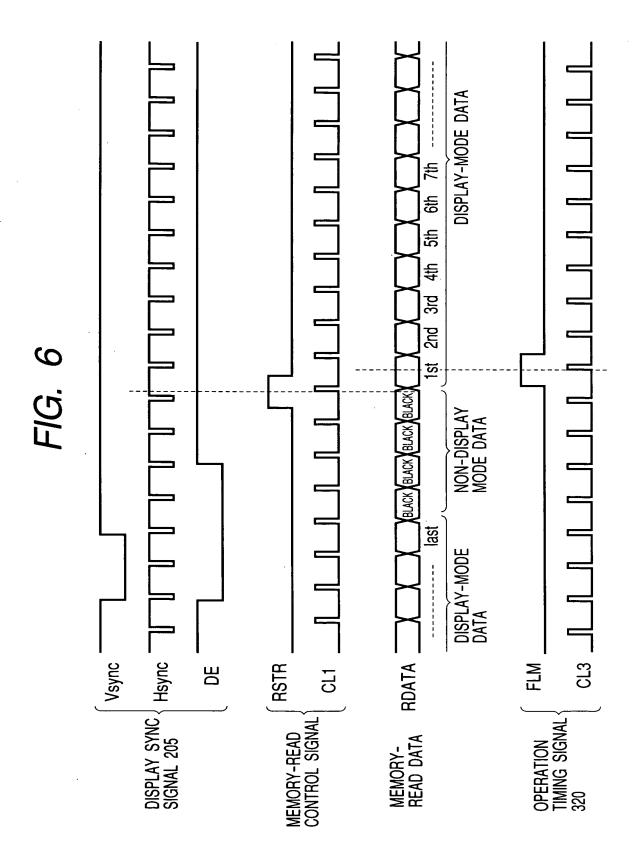
MS = SIGNAL REPRESENTING NUMBER OF DRIVE LINES IN MAIN SCREEN, SL = SIGNAL REPRESENTING NUMBER OF DRIVE LINES IN SUB-SCREEN, BL = SIGNAL REPRESENTING NUMBER OF LINES IN VERTICAL BLANKING PERIOD,

\* = UNESTABLISHED

	SO	
ESTABLISHED VALUE	0	-
OPERATION OF DRIVE CIRCUIT	MAIN SCREEN IS IN DISPLAY MODE (SUB-SCREEN IS IN DISPLAY MODE (SUB-SCREEN IS IN NON-DISPLAY MODE)	SUB-SCREEN IS IN DISPLAY MODE (MAIN SCREEN IS IN NON-DISPLAY MODE)

FIG. 5





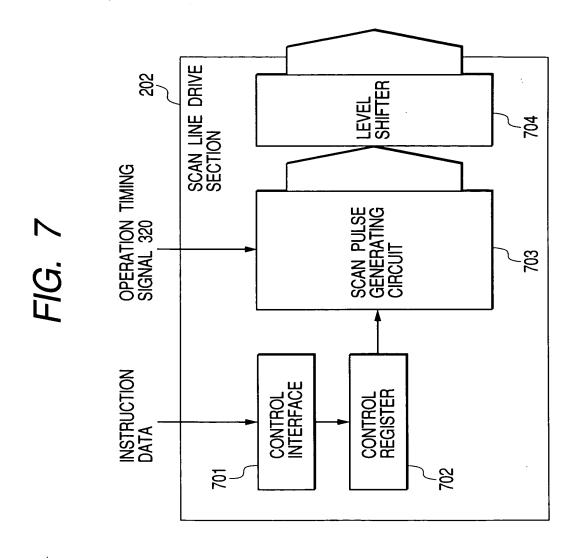


FIG. 8

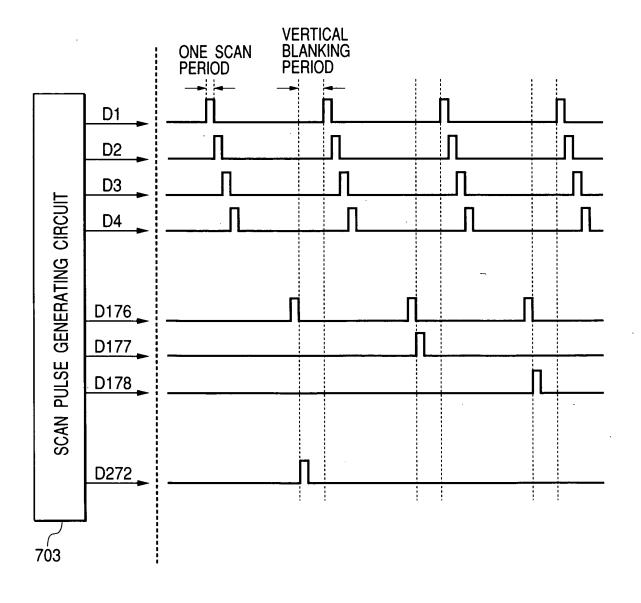


FIG. 9

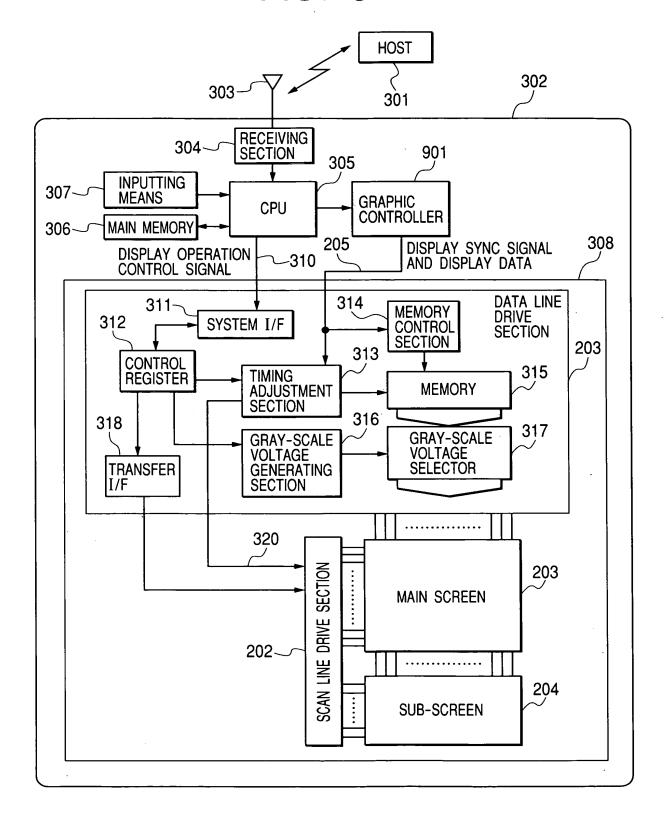


FIG. 10

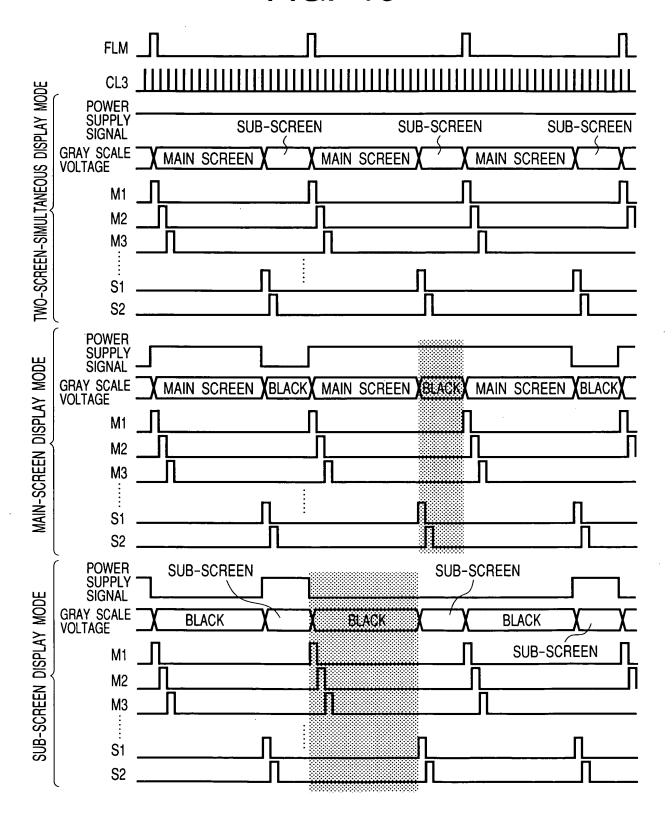


FIG. 11

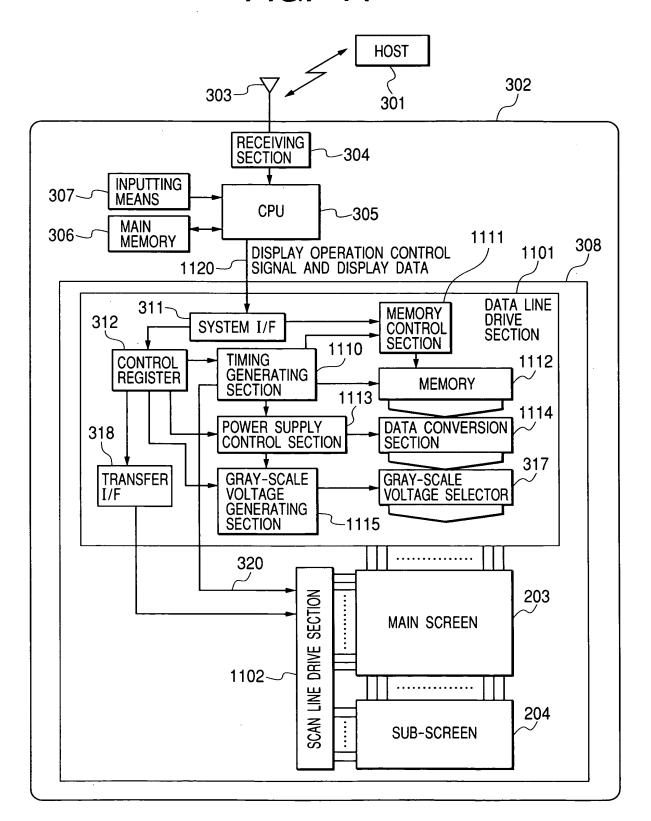


FIG. 12

